

FIG. 1

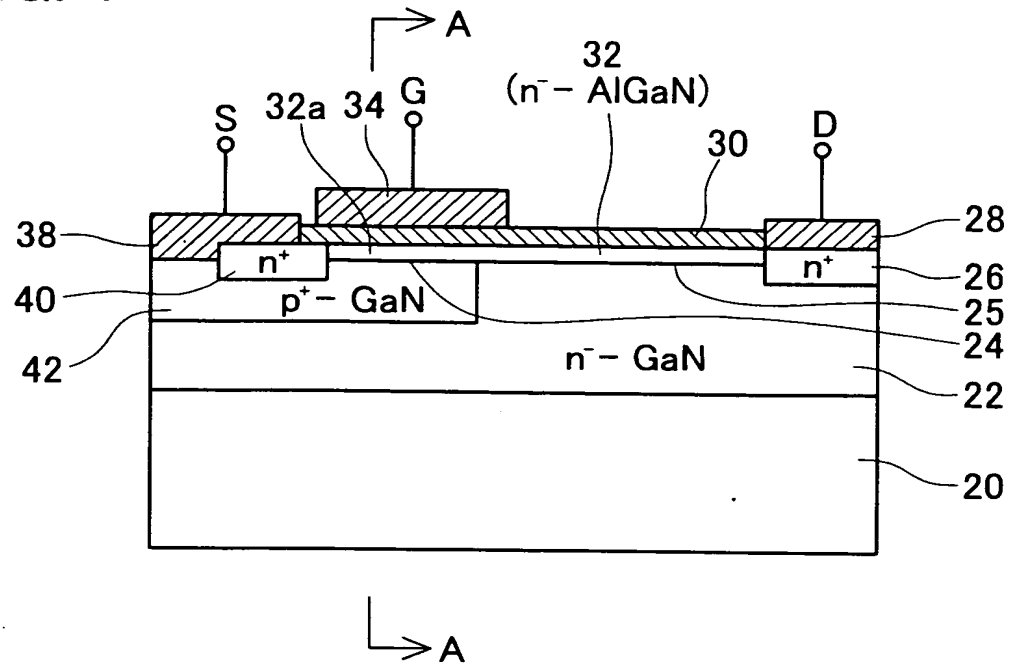


FIG. 2

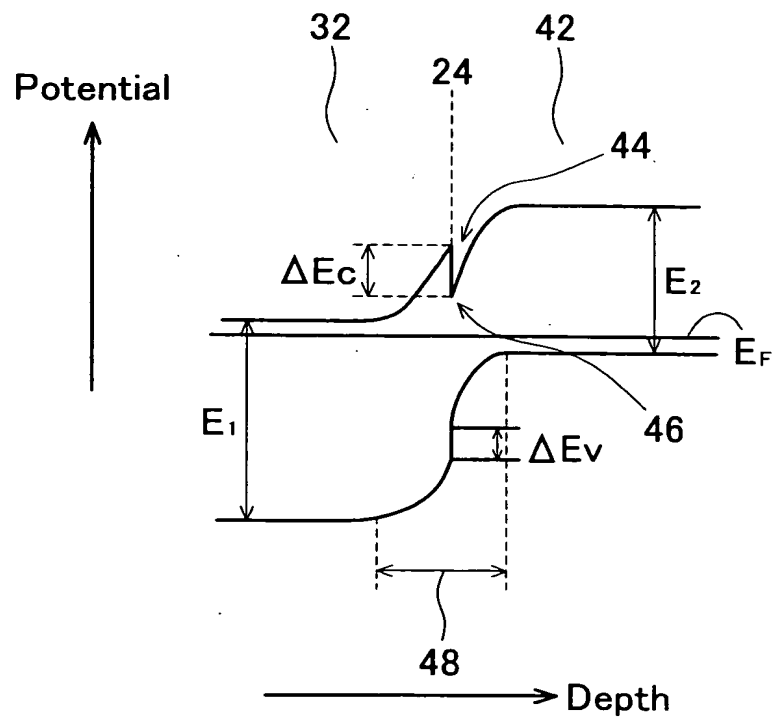


FIG. 3

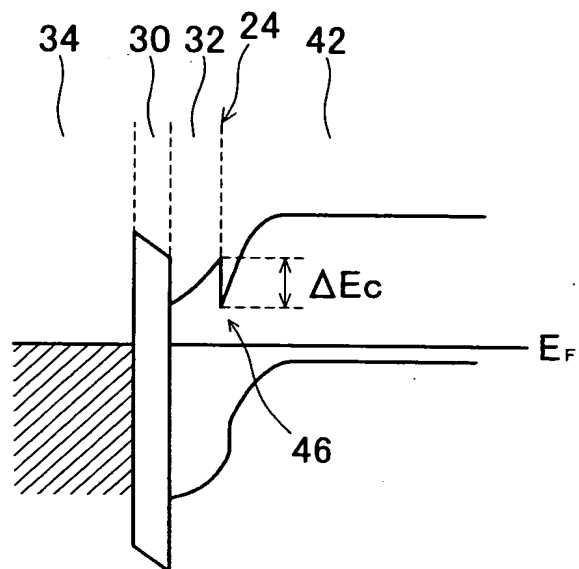
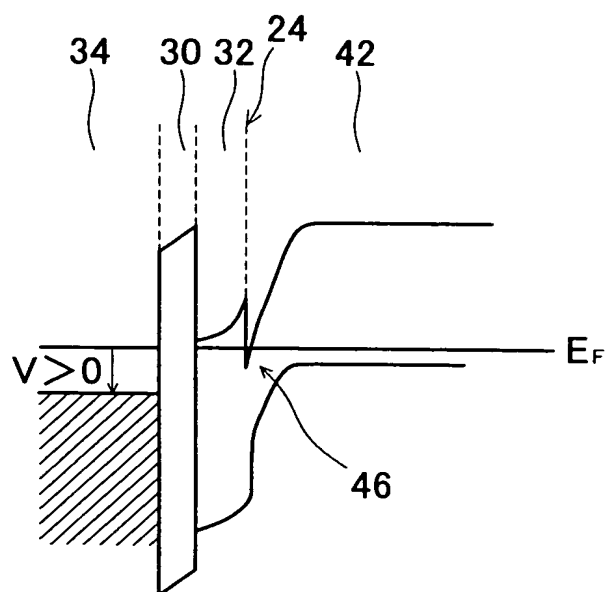


FIG. 4



This diagram shows the energy levels of a semiconductor device. A horizontal line represents the Fermi level, labeled E_F . The device consists of a substrate (34) on the left, a channel region (30) in the middle, and a gate region (42) on the right. The channel region (30) is divided into a source region (32) and a drain region (44). The energy levels are shown as a function of position, with the source region (32) having a lower energy level than the drain region (44). The gate region (42) has a higher energy level than the channel region (30). The energy level in the source region (32) is labeled 49.



FIG. 9

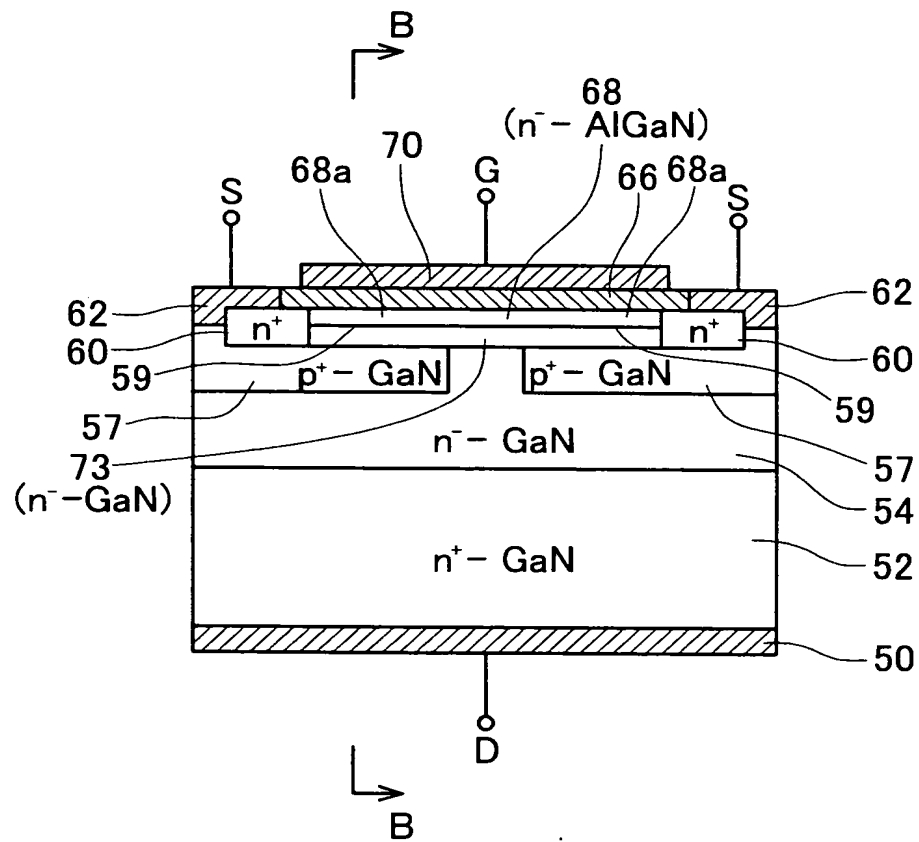


FIG. 10

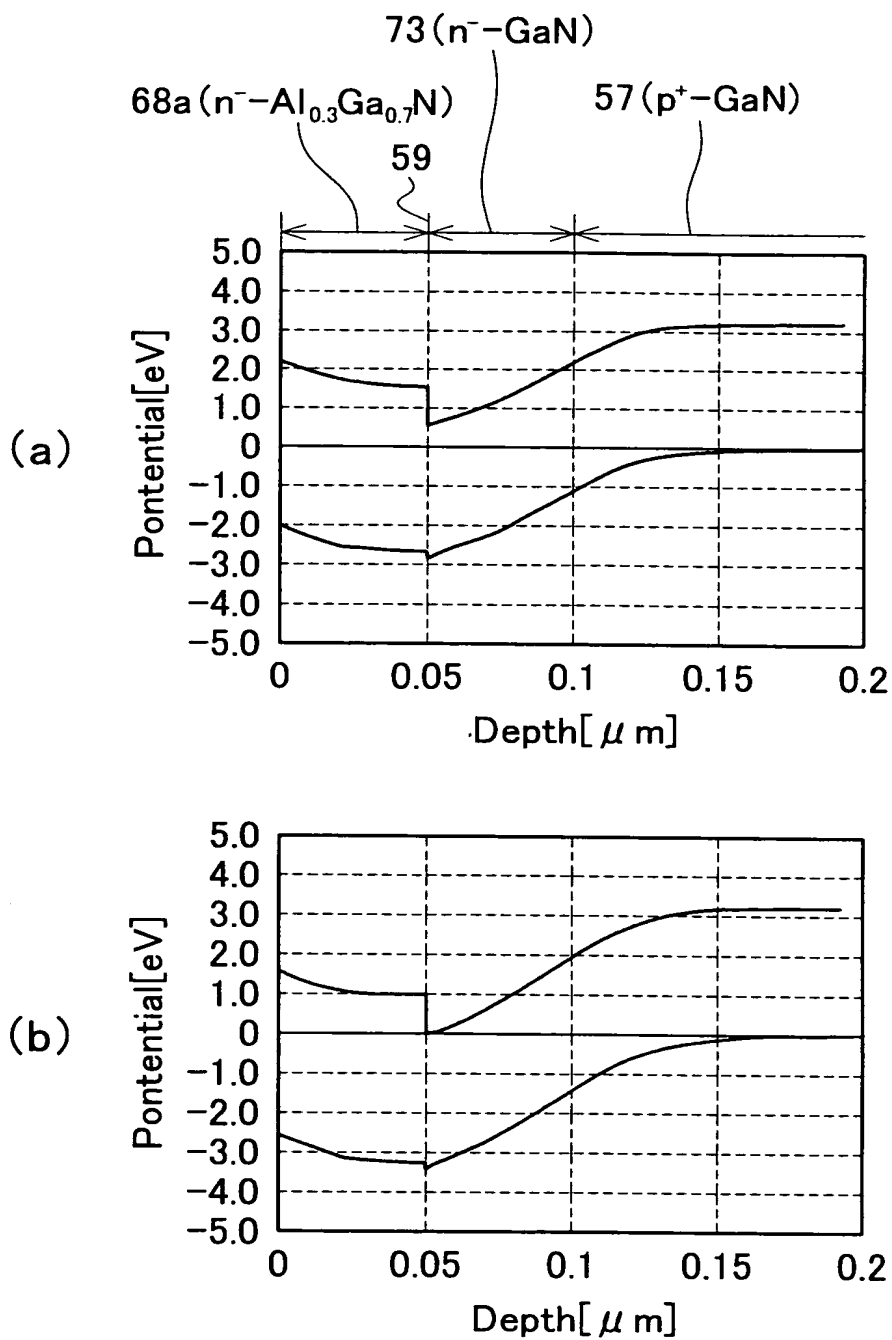


FIG. 11

